



AMENDMENTS TO THE SPECIFICATION

Please replace paragraph [0053] with the following paragraph:

In the present invention, to print very narrow gates, e.g., of 32nm width, a rectangular monopole illumination mode IM formed within a field F is used, as shown in Figure 3A. The rectangular monopole is effectively identical to a circular monopole (Figure 3B) with very low σ if the following condition is met:

$$0.404 \cdot R = B/4$$